



ITW

Docket No.: M4065.0901/P901  
(PATENT)

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of:  
Hagop A. Nazarian

Application No.: 10/699,652

Confirmation No.: 3940

Filed: November 4, 2003

Art Unit: 2183

For: SERIAL TRANSISTOR-CELL ARRAY  
ARCHITECTURE

Examiner: Not Yet Assigned

**STATUS INQUIRY**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

It is respectfully requested that the attorney named below be advised of the status of the above-identified application. Please advise us of when we might expect to receive an Office Action from the Patent and Trademark Office.

Dated: November 4, 2004

Respectfully submitted,

By 

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